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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/082,176	02/26/2002	Toshitaka Hasegawa	826.1796	2408
21171	7590	01/07/2005	EXAMINER	
STAAS & HALSEY LLP SUITE 700 1201 NEW YORK AVENUE, N.W. WASHINGTON, DC 20005			PERVEEN, REHANA	
			ART UNIT	PAPER NUMBER
			2116	

DATE MAILED: 01/07/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Applicant No.	Applicant(s)	
	10/082,176	HASEGAWA, TOSHIKATA	
	Examiner Rehana Perveen	Art Unit 2116	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 26 February 2002.
- 2a) This action is FINAL. 2b) This action is non-final.
- 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-15 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) Claim(s) _____ is/are allowed.
- 6) Claim(s) 1-15 is/are rejected.
- 7) Claim(s) _____ is/are objected to.
- 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
- 10) The drawing(s) filed on 26 February 2002 is/are: a) accepted or b) objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>2/26/02</u> . | 6) <input type="checkbox"/> Other: _____. |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Budnik et al, Patent No. EP0499564A2.

As to claims 1 and 2, Budnik et al teach a power supply control device provided for each of a plurality of information processing devices connected to a network (col. 3 lines 21-29), an arbitrary information processing device of the plurality of information processing devices issuing a power-up instruction to each power supply control device upon each activation according to a predetermined power-up/power-down schedule (col. 3 line 18 – col. 4 line 42 and col. 7 lines 39-41, inherent for scheduled power-on time), instructing each of the information processing devices to perform a power-down process, notifying the information processing devices of a next power-up date and time, and having each power supply control device enter a next power-up date and time each time a power-down date and time comes (col. 4 lines 29-42), and each power supply control device performing a power-up process upon receipt of the power-up instruction or when the entered power-up date and time comes (figures 1 and 2).

As to claims 3 and 4, Budnik et al teach the power-up date and time given to each of the power supply control devices is obtained by any of the information processing devices adding an arbitrary margin to a power-up date and time in the predetermined power-up/power-down schedule (abstract and col. 3 line 42 – col. 4 line 7).

As to claims 5 and 6, Budnik et al teach the arbitrary information processing device does not give the power-down instruction and the next power-up date and time before a power-down permission condition entered in advance of a current and other information processing devices is satisfied although the power-down date and time comes (col. 2 lines 26-30).

As to claims 7 and 8, Budnik et al teach the power-up instruction or the power-down instruction is sequentially issued at predetermined startup intervals or power-down intervals (col. 4 lines 37-42).

Claims 9-13 are directed to the method of system claims 1-8, claim 14 is directed to the computer readable medium of system claims 1 and 2, and claim 15 is directed to the computer data signal of system claims 1 and 2. Budnik et al teach the system as set forth in claims 1-8. Therefore, Budnik et al also teach the method as set forth in claims 9-13. Further, Budnik et al teach the computer readable medium claim as set

forth in claim 14. Yet further, Budnik et al teach the computer data signal as set forth in claim 15.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Rehana Perveen whose telephone number is 571-272-3676. The examiner can normally be reached on 8:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynne H Browne can be reached on 571-272-3670. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Rehana Perveen
Primary Patent Examiner
Technology Center 2100